TSB75T100S(A)S-255B

5A/100V, low VF Schottky barrier diode with trench MOS structure

Mechanical Data

Chip Drawing	Item	Information	
	Die Size (A)	1905 μm	75 mil
	Top Metal Pad Size (B)	1812μm	71mil
	Chip Size (C)	1825μm	72mil
	Wafer Thickness (D)	255 μm	9.5 mil
	Scribe Line Width (E)	80 μm	3.15 mil
	Wafer Size	6 inch	
	Top Side Metallization	Al/Ag	
	Back Side Metallization	Ti Ni Ag	
	Recommended Storage Environment	Stored in original container, in dry nitrogen, (6 months at an ambient temperature of 23℃±3℃)	

Electrical Characteristics (T$_J$=25℃, unless otherwise specified)[1]

Parameter	Description	Min.	Typ.	Max.	Unit	Test Condition
V_{BR}	Reverse Breakdown Voltage	105	110	-	V	I_R =100μA
V_F	Instantaneous Forward Voltage	-	0.55	0.58	V	I_F =5A[3]
I_R	Reverse Leakage Current	-	10	40	μA	V_R =105V
T_J, T_{STG}	Operating and Storage Temperature	-40℃ to 150℃ Max				

Note:

(1) The preliminary wafer datasheet only for reference;

(2) This characteristics assumes the dies are assembled in SMB packages. Actual performance may degrade when assembled. YJ does not guarantee device performance after assembly;

(3) Pulse Width tp = < 300μS, Duty Cycle <2%;